

Photomasks Data Preparation

Training Institution : XYALIS

Authors: Eric Beisser, Farid Benzakour

Objective

Main goal of this course is to present the techniques used in micro and nano components photomask data preparation. This course will allow to fully understand all the issues related to this critical step of chip development as well as the methods used to reduce both costs and delays.

Abstract

Despite the fact that all nowadays microchips use photomasks for their fabrication, it appears that no course is available worldwide on this topic. The only available training are provided internally in companies dealing with this activity and without any formal teaching. This leads to dramatic discrepancies in the wording used by the professionals as well as an important weakness regarding efficiency and homogeneity of the procedures. It is then important to correct this lack by providing an up to date course on this topic, especially when knowing that masks cost represents today at least 10% of the full development cost of a chip.

Contents

The course introduces the basics of photolithography and the various types of masks used. A detailed description of the multiple constraints and of all the patterns required on masks will be provided:

- process control modules
- measurement structures
- alignment marks
- identification patterns
- chips
- inspection techniques
- maintenance/repair techniques

The different steps of mask data preparation will be explained:

- MDP CAD flow
- Input data manipulation
- validation
- fracturing
- OPC (optical Proximity Correction)
- assembly of various devices
- Mask layout design
- constraints and optimization
- Mask ordering procedures

A detailed and comprehensive glossary providing a clear definition of each technical term used within this specific domain will be appended to the course

Learners

The primary target groups concerned are: professionals from SMEs in electronics and micro-systems, students in engineering education and vocational schools looking for an advanced knowledge in this critical step and a better understanding of all related issues.

Prerequisites

- Basic knowledge in microelectronics
- design
- lithography

It is assumed also that learners have a basic knowledge in physics (electronics and optics).